

Title: HIGH SPEED SOFTWARE DRIVEN EMULATOR COMPRISED OF A PLURALITY OF EMULATION PROCESSORS WITH A METHOD TO ALLOW MEMORY READ/WRITES WITHOUT INTERRUPTING THE EMULATION

5 Abstract of the Disclosure:

A software driven emulator in which the stored emulation program for a processor module is compiled to include a code bit or bits in the emulation instruction step sequence that is decoded as main data memory disable command. Thus, once in each emulation 10 program cycle, the memory controller disables the main data memories on the module, and allows the maintenance bus to read or write data to these memories.

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